

GHARDA INSTITUTE OF TECHNOLOGY

Department of Electronics and Telecommunication.

Mini Project 1A SE EXTC 2021-22

(Affiliated to Mumbai University & Approved by AICTE, New Delhi, DTE, Maharashtra)

At & Post: Lavel, Taluka: Khed, District: Ratnagiri,

Pin: 415708, Maharashtra State

Tel: 91-2356-262795-99, Fax: 91-2356-262980

Electronics and Tele-communication

Mini Project 1 A SE EXTC 2021-22

SE- Sem IV Mini Project Group 2020-21

Group Number	Name of Students	Branch/Class	Final Project Topic	Project Internal Guide
ET1	5_Bhosale Mrunmayi Santosh	E&TC/SE-III	Simple Water Level Indicator	Prof. S. R. Khedekar
	7_Chavan Prajakta Nagesh	E&TC/SE-III		
	13_Joshi Rudali Rajendra	E&TC/SE-III		
	15_Kadam Vanshita Sunil	E&TC/SE-III		
ET2	19 KONDEKAR MUDASSIR RAFIK	E&TC/SE-III	Verification of NAND gate using two transistor	Prof. S.N.Pandharkame
	22 MULLA TOHID MAQSOOD	E&TC/SE-III		
	20 Mahind Harshal Anna			
	25 PATIL ANKIT RAGHUNATH	E&TC/SE-III		
ET3	2 Tejas Prakash Ambre	E&TC/SE-III	Verification of AND gate using two transistor BC547	Prof. S.N.Pandharkame
	16 = Abhinit Baliram kalambate	E&TC/SE-III		
	6 = Gautam Shivilal Choudhary	E&TC/SE-III		
ET4	3 Rahul Rajendra bamane	E&TC/SE-III	12v battery charger circuit with auto cut off	Prof. M. A. Mohite
	21 Omkar Rajesh	E&TC/SE-III		
	24 Tejas hemchandra padte	E&TC/SE-III		

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Mini Project coordinator

Dr. Sachin R. Khedekar

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Group Number	Name of Students	Branch/Class	Final Project Topic	Project Internal Guide
ET5	1 Kangutkar gauri santosh	E&TC/SE-III	Verification of SR flip flop using Nand gate IC 7400	Prof. S. K. Muchande
	2)Gotal kiran prabhakar	E&TC/SE-III		
	3)kangutkar girija santosh	E&TC/SE-III		
	26 Patil Mayuri	E&TC/SE-III		
ET6	11-Humdulay Taha	E&TC/SE-III	Verification of OR gate using two transistor BC 547	Prof. S.C.Munghate
	09-Milind Dhade	E&TC/SE-III		
	01-Akshay Ambekar	E&TC/SE-III		
ET7	12 Ingavale Chinmayee Arun	E&TC/SE-III	Verification of Demorgans first theorem using logic ICs	Prof. S.C.Munghate
	27 Patil Siddhi	E&TC/SE-III		
	31 Telange Mansi	E&TC/SE-III		
ET8	8_Dalvi Saurabh	E&TC/SE-III	Verification of Demorgans second theorem using logic ICs	Prof. V. S. Kolge
	29_Shaikh Shams	E&TC/SE-III		
	32_Utekar Sanket	E&TC/SE-III		
	33_ware Sairaj	E&TC/SE-III		

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Mini Project coordinator
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Group Number	Name of Students	Branch/Class	Final Project Topic	Project Internal Guide
ET9	4_Vishal Benake	E&TC/SE-III	LED flasher using BC547 (DJ light)	Prof. S. R. Khedekar
	30_Sahil Surv	E&TC/SE-III		
	14_Mauli Kadam	E&TC/SE-III		
DSE1	Parkar Alkama Dawood ,	E&TC/SE-III	Dual led flash by using transistor	Prof. S. R. Khedekar
	Rohan Shinde,	E&TC/SE-III		
	Lad Siddhesh	E&TC/SE-III		
DSE2	More Anjali kumar	E&TC/SE-III	Dual LED Flasher	Prof. S. R. Khedekar
	Sanika Arvind Burmadkar	E&TC/SE-III		
	Utteja Uday Rajshirke	E&TC/SE-III		
DSE3	Dalvi Shreyas Digambar	E&TC/SE-III	Dual led flash by using transistor 2n2222a.	Prof. S. R. Khedekar
	Kolge Om Moreshwar	E&TC/SE-III		
	Sawardekar Shubham Kiran	E&TC/SE-III		

Mini Project coordinator

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